| Ousmane Toure & Jianning Chen  EECE2160 | Embedded Design: Enabling Robotics  Prelab Assignment 8 |
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Prelab Assignment 8

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**5.0 Prelab Questions:**

The Start and end address for this lab is 0xC8000000 to 0xFF203020. The 0xC8000000 address is the starting base address of the DE1-SoC computer, To be able to access and output onto the VGA monitor, we need to access the pixel buffer base at address 0xFF203020. This was found by reading the ARM 9 manual and the provided header file to figure this out. The manual specifies that the control base buffer is what displays the pixel values so our end address should end here.

# References

1. Prof. Julius Marpaung, “*Lab Report Guide*”, Northeastern University, January 6 2020.